Abstracts

Broadband High-Efficiency HBT MMIC Power Amplifier Fabricated with Re-Aligned Process

J.J. Komiak, L.W. Yang, R.S. Brozovich, S.T. Fu and D.P. Smith. "Broadband High-Efficiency HBT MMIC Power Amplifier Fabricated with Re-Aligned Process." 1993 MTT-S International Microwave Symposium Digest 93.3 (1993 Vol. III [MWSYM]): 1473-1476.

The design and performance of a 6 to 10 GHz Heterojunction Bipolar Transistor MMIC power amplifier that produces 1.25 to 2.15 Watts at 30 to 46 % power-added efficiency with 9.5 to 12.5 dB of power gain will be described. With only 500 µm² of output periphery, record MMIC HPA power densities of 2.5 to 4.3 mW/µm² have been demonstrated.

❑ Return to main document.